

1. input \square SSL
 1. \square SSL \square input \square
2. output \square BL
 1. \square BL \square output \square 1 \square
 2. 8 \square BL \square 8 \square
 3. BL \square ADC \square bit
 \square
3. weight \square cell \square
4. WL \square

??MAC???

1. \square MNK
2. SSL input: \square input \square input \square K \square
 1. \square input \square K
3. BL output \square BL \square input \square \square weight \square N \square 1
 \square
 1. \square output \square N
4. WL weight \square index
 1. \square N \square bit index

[![[b81dc059-2002-4e2b-932e-073924c856af.jpg]](Nand flash/b81dc059-2002-4e2b-932e-073924c856af.jpg)](Nand flash/b81dc059-2002-4e2b-932e-073924c856af.jpg)