

Sync And Async

?????

1. []
2. []
3. N to N []
4. []
5. [] pulling [] latency
6. []
 1. transformer [] Flash-attention [] L1 [] fusion [] DMA []
7. []

??Global????????????????

1. []
2. []
3. []
 1. DMA [] Kernel []
4. []

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1. [] Sync [] mailbox [] N to N []
2. [] producer consumer [] Pipeline
3. []
 1. []
 2. []
4. []
5. []
 1. [] write mbx [] wait mbx
[] ack [] latency []

???????????? mbarrier

1. [] M barrier
[]
 1. []
 2. []
2. []
 1. []
[]
 2. [] fence
 3. [] Core0 Store() Core0 Fence() memory Ack() Core0 Signal(Core1 mbx)

- 4. [] Core0 Store_mbarrier() Core1 Wait(mbarrier)
 - 3. []
 - 1. []
 - 4. []
 - 1. [] wait[] local[] mbarrier[] counter[] latency[]
 - 2. [] transaction[]
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