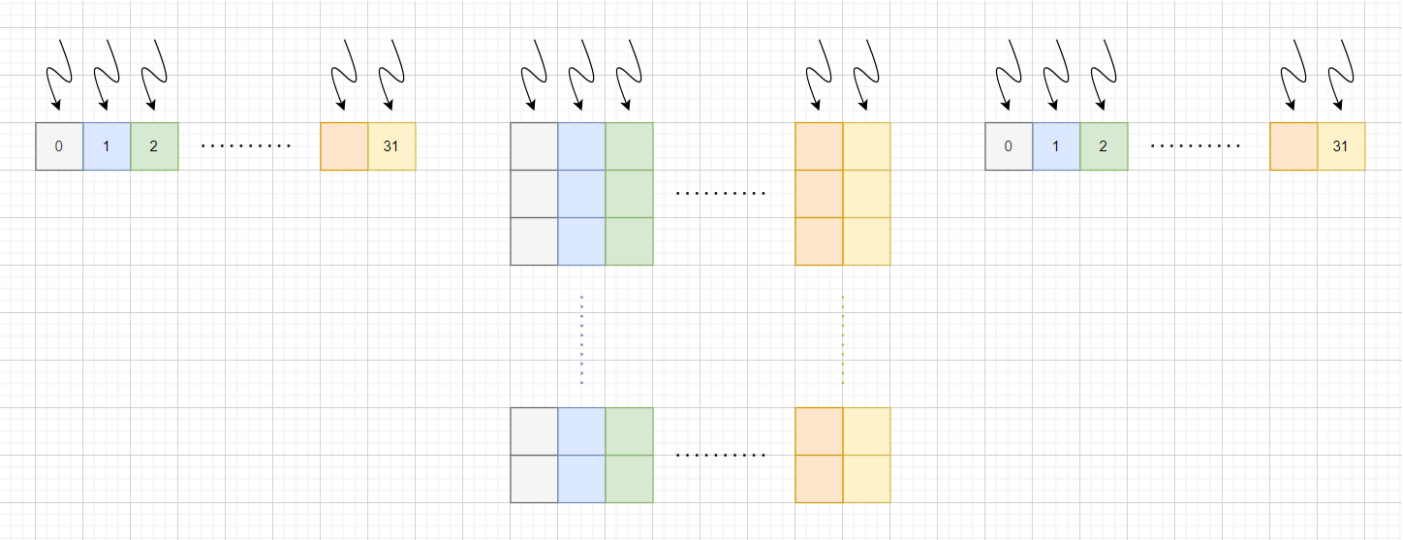

1. DMA 2D算力的表达和设计
2. 微架构和 ISA的配合，软件控制流水线，硬件 hzd检查简单高效
3. GS和Cache系统的设计
1. 左右支的复用
4. RO WO 存储类型的利用

SIMT With Vector

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