AI???????

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1. [illegible] ： Dot / GEMM => Join+Reduce
2. [illegible] ： ElementWise Broadcast
3. [illegible] ： Reduce ： G/S
4. [illegible]
5. [illegible]
    1. [illegible] +[illegible]
    2. [illegible] imm[illegible]
    3. [illegible]
    4.
    ```
    /* emulate GPU's LOP3.LUT (three-input logic op with 8-bit truth table) */
    uint32_t lop3_fast (uint32_t a, uint32_t b, uint32_t c, uint8_t ttbl)
    {
        uint32_t r = 0;
        if (ttbl & 0x01) r |= ~a & ~b & ~c;
        if (ttbl & 0x02) r |= ~a & ~b &  c;
        if (ttbl & 0x04) r |= ~a &  b & ~c;
        if (ttbl & 0x08) r |= ~a &  b &  c;
        if (ttbl & 0x10) r |=  a & ~b & ~c;
        if (ttbl & 0x20) r |=  a & ~b &  c;
        if (ttbl & 0x40) r |=  a &  b & ~c;
        if (ttbl & 0x80) r |=  a &  b &  c;
        return r;
    }
    ```

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1. [illegible] [illegible] [illegible]
    1. [illegible]
2. [illegible]

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1. [illegible]
2. [illegible] throughput
3. [illegible] latency[illegible]
    1. [illegible] scoreboard， forwarding[illegible] pipeline[illegible]
    2. [illegible] mailbox， barrier[illegible]

4. [illegible]
5. [illegible]
6. [illegible] ([illegible] roq [illegible] sram [illegible]) [illegible]
7. [illegible]

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1. [illegible]
    1. [illegible]
    2. [illegible]
    3. [illegible]
2. [illegible]
    1. [illegible]
    2. [illegible]
    3. [illegible]
3. [illegible]

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1. [illegible]
2. [illegible]
    1. latency [illegible]
    2. [illegible] “[illegible]” [illegible], [illegible]
    3. [illegible] buffer [illegible] footprint, [illegible]
    4. [illegible]
    5. PC [illegible]
    6. [illegible]
        1. [illegible]
        2. [illegible]
        3. latency [illegible]

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1. [illegible] MMU
2. [illegible]
3. [illegible] cycle [illegible]
4. [illegible]
    1. [illegible] 2 [illegible]
    2. [illegible] hash [illegible] mmu [illegible]
    3. [illegible] cuda [illegible] LOP3.LUT [illegible]
    4. [illegible]

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